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chapter, many so-called  
"linear" circuits are  
quite nonlinear in their  
behavior, either by the  
necessity of physics or  
by design. The circuits  
in this chapter make use  
of IC, ...

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but they do affect the  
overall design. The first  
is the  $V_{be}$  drop. For a

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transistor, this should be about 0.6 or 0.7 volts, dependant on temperature. If the real circuit or an ...

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For this device, a turn-off gate voltage of 0V is sufficient to prevent PTO even at voltage

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slopes of 50 V/ns and temperature of 175°C, thus simplifying the design of the gate drive circuit. Figure 3 ...

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Analog circuit and  
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system design today is more essential than ever before. With the growth of digital systems, wireless communications, complex industrial and automotive systems, designers are being challenged to develop sophisticated analog solutions. This comprehensive source book of circuit design

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number of examples of state-of-the-art asynchronous systems to illustrate what can be built using asynchronous techniques. The examples range from a complete commercial smart card chip to complex microprocessors. The objective in writing this book has been to enable

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therefore, describes the structured electronic design of high-performance harmonic oscillators and bandgap references. The structured design of harmonic oscillators includes the maximization of the carrier-to-noise ratio by means of tapping, i.e. an impedance adaption method for noise

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matching. The bandgap reference, a popular implementation of a voltage reference, is studied via the unusual concept of the linear combination of base-emitter voltages. The presented method leads to the design of high-performance references in CMOS and Bipolar technology. Using this concept, on a high level

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of abstraction the quality with respect to, for instance, noise and power-supply rejection can be identified. In this book, it is shown with several design examples that this method provides an excellent starting point for the design of high-performance bandgap references. Auxiliary to the harmonic-oscillator

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and bandgap reference design are the negative-feedback amplifiers. In this book the systematic design of the dynamic behavior is emphasized.

By means of the identification of the dominant poles, it is possible to give an upper limit of the attainable bandwidth, even before the real frequency compensation

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is accomplished.

Structured Electronic Design: High-Performance Harmonic Oscillators and Bandgap References is a valuable book for researchers and designers, as well as students in the field of analog design. It helps both the experienced and trainee designer to come to grips with the design of analog

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circuits. The presented method is illustrated by several well- described design examples.

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filtering function on  
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reduce system cost and

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provide greater accuracy. Part III, Smart Power, illustrates up-to-date techniques for implementing thermal detectors and protection networks to improve reliability and the lifetime of many analog devices. These devices are more specifically those with different analog blocks operating at different

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