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AN928.2: EFR32 Series 2 Layout Design Guide

High-Speed Interface Layout Guidelines 1 Introduction 1.1 Scope This application report can help system designers implement best practices and understand PCB layout options when designing platforms. This document is intended for audiences familiar with PCB manufacturing, layout, and design. 1.2 Critical Signals

High-Speed Interface Layout Guidelines (Rev. H)

ABSTRACT As modern interface frequencies scale higher, care must be taken in the printed circuit board (PCB) layout phase of a design to ensure a robust solution. This document focuses on high speed layouts guidelines relating to USB, USB Hubs, HDMI, DisplayPort, PCIe and SATA.

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Base Plate and Anchor Rod Design

AN928.1: EFR32 Series 1 Layout Design Guide The purpose of this application note is to help users design PCBs for the EFR32 Wireless Gecko Portfolio using design practices that allow for good RF performance. The 2.4 GHz matching principles are described in the application note, AN930.1:

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